Abstract of the Disclosure

A flat panel display includes a vacuum envelope, a cathode, a gate electrode substrate, and a phosphor screen and anode electrode. The vacuum envelope includes a front glass plate which is at least partly transparent and a substrate opposing the front glass plate. The cathode is formed on the substrate and has an electron-emitting source. The gate electrode substrate has an electron-passing hole and is arranged in the vacuum envelope to oppose the substrate to be separate from the cathode. The phosphor screen and anode electrode are formed on a surface of the front glass plate in the vacuum envelope. The gate electrode substrate includes at least an insulating substrate and a gate electrode. The insulating substrate has the electron-passing hole. The gate electrode is formed on the insulating substrate. A method of manufacturing a flat panel display is also disclosed.